



2829

[02207/10551]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : S. SAMAAAN et al.
Serial No. : 09/855,050
Filed : May 14, 2001
For : **A DEVICE AND METHOD FOR PROBING
INSTANTANEOUS HIGH-SPEED LOCAL SUPPLY
VOLTAGE FLUCTUATION IN VLSI INTEGRATED
CIRCUITS USING IR EMISSIONS**
Art Unit : 2829
Examiner : Jimmy Nguyen

Commissioner for Patents
Washington, D.C. 20231

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RESPONSE TRANSMITTAL

SIR:

Transmitted herewith for filing in the above-identified patent application is a Response to an Office Action.

While no fees are believed to be due, the Commissioner is authorized, as appropriate and/or necessary, to charge any fees (including any Rule 136(a) extension fees) or credit any overpayment to Deposit Account No. 11-0600. A duplicate copy of this transmittal letter is enclosed for this purpose.

Respectfully submitted,
KENYON & KENYON

Dated: 9/29/03

By: Howard I. Grossman
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Signature: Elizabeth Whit

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[2207/10551]

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RESPONSE TO OFFICE ACTION

Signature: Abi Sheik V. L. T.

SIR:

In response to the Office Action mailed on August 1, 2003, please reconsider the
above-identified application based on the following.

REMARKS

Claims 1 to 18 are currently pending in the present application.

Claims 1-18 have been rejected under 35 U.S.C. § 102(e) as being anticipated by
U.S. Patent No. 6,496,022 to Kash et al. ("Kash '022"). Kash '022 incorporates by
reference a previously granted patent having the same lead inventor, U.S. Patent No.
5,940,545 ("Kash '545").

In order to reject a claim under 35 U.S.C. § 102, the Office must demonstrate
that each and every limitation is identically disclosed in a single prior art reference. See
Scripps Clinic & Research Foundation v. Genentech, Inc., 18 U.S.P.Q.2d 1001, 1010
(Fed.Cir. 1991). "The identical invention must be shown in as complete detail as is
contained in the claim." M.P.E.P. § 2131.

Independent claim 1 recites an apparatus comprising a voltage probe
incorporated in a VLSI circuit and coupled to a source of a local supply voltage, the

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